FIG. 1

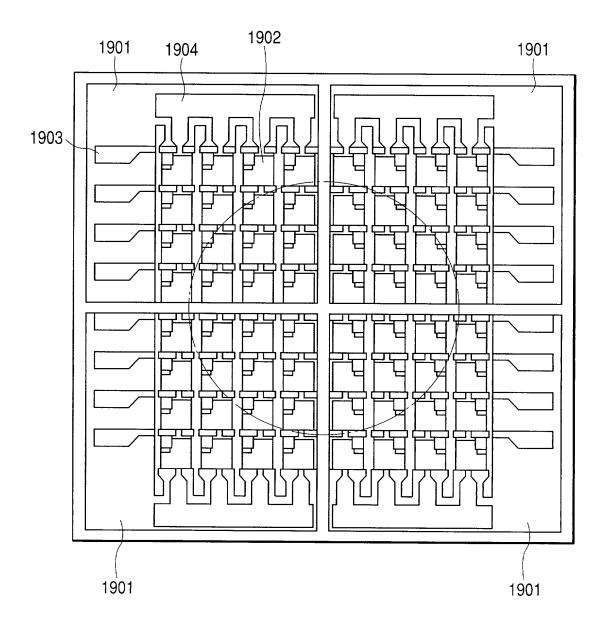
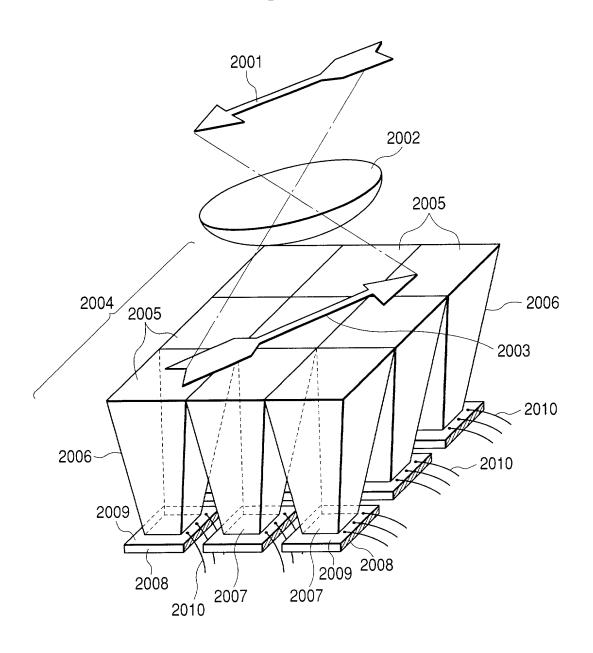
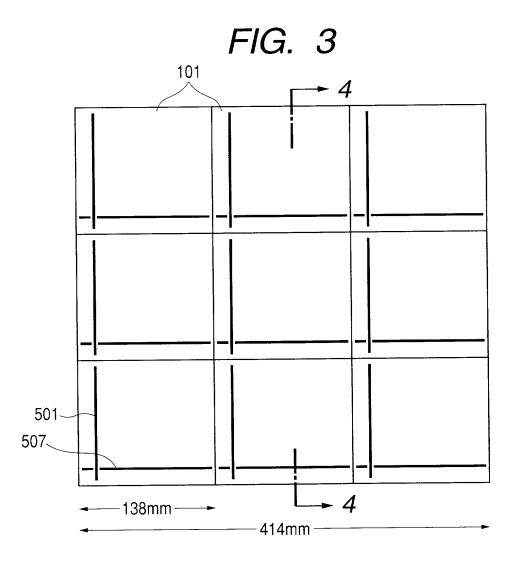
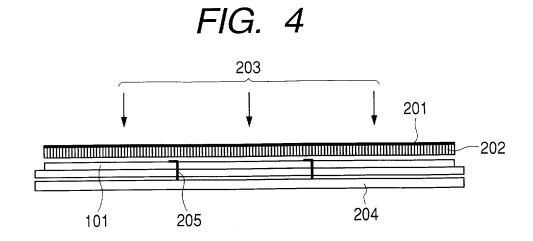


FIG. 2







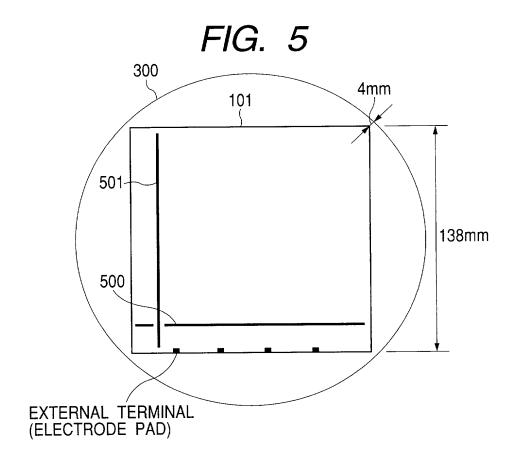


FIG. 6

ΦTX ΦRES ΦSEL

402

403

ONE PIXEL CIRCUIT

SIGNAL OUTPUT LINE

FIG. 7

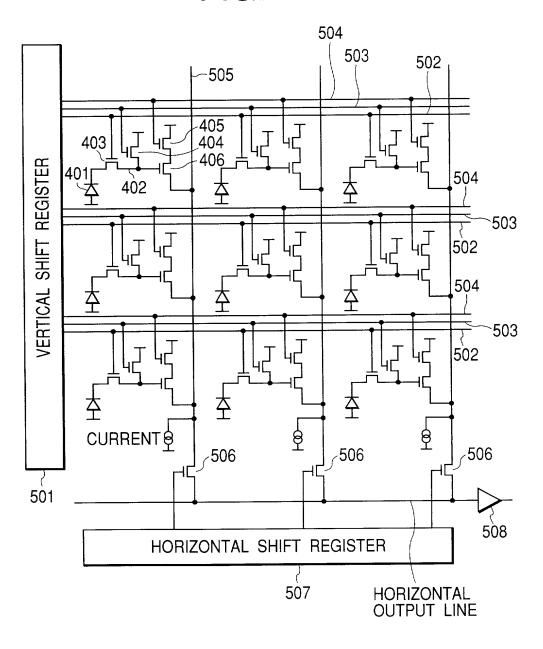


FIG. 8

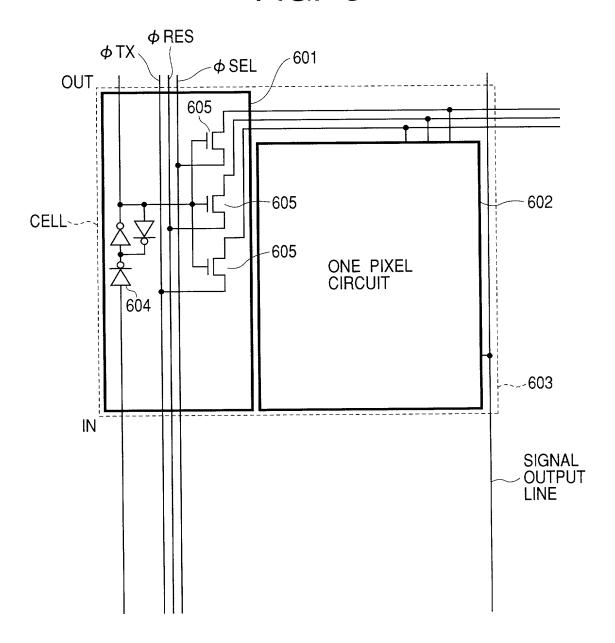


FIG. 9

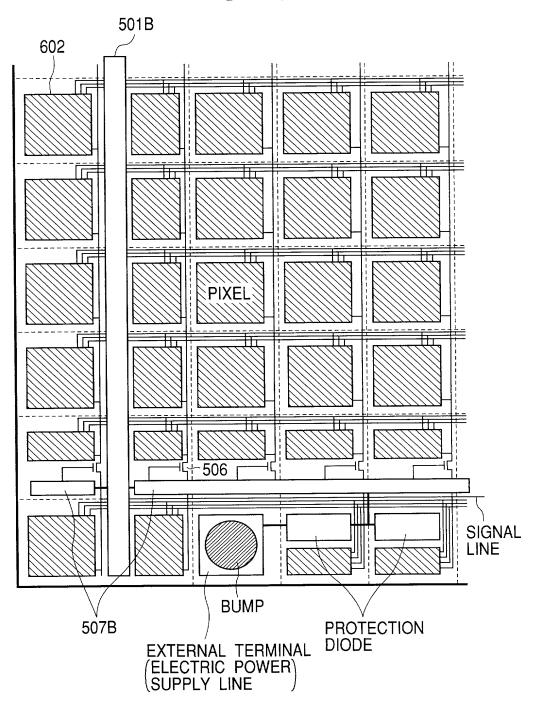


FIG. 10

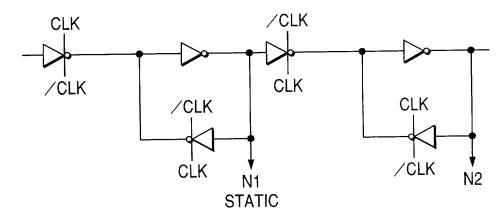


FIG. 11

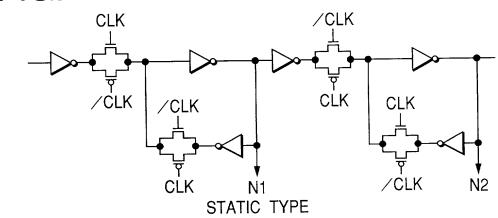
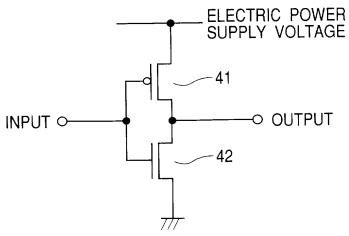


FIG. 12



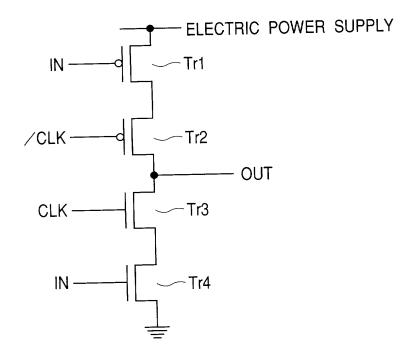


FIG. 14

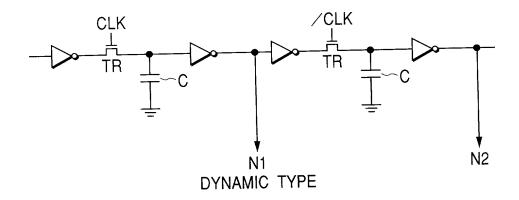
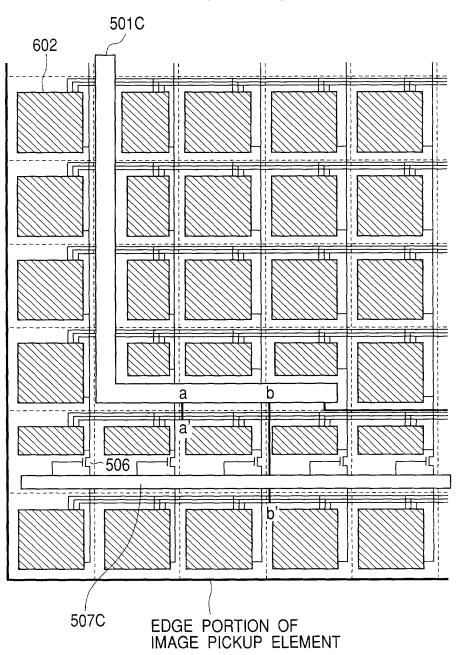
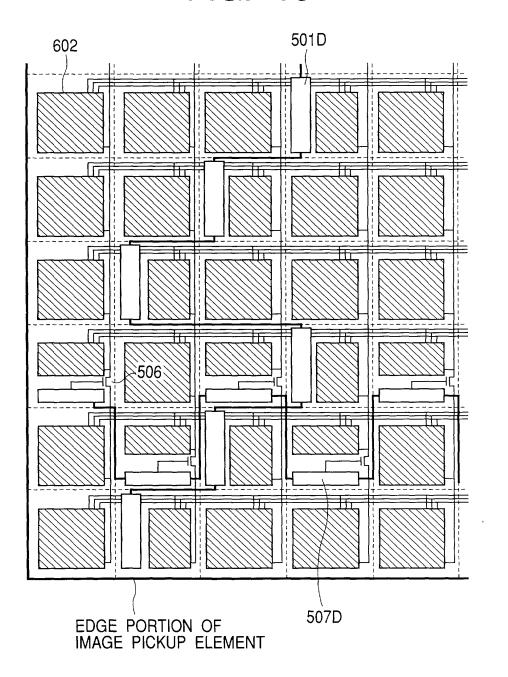
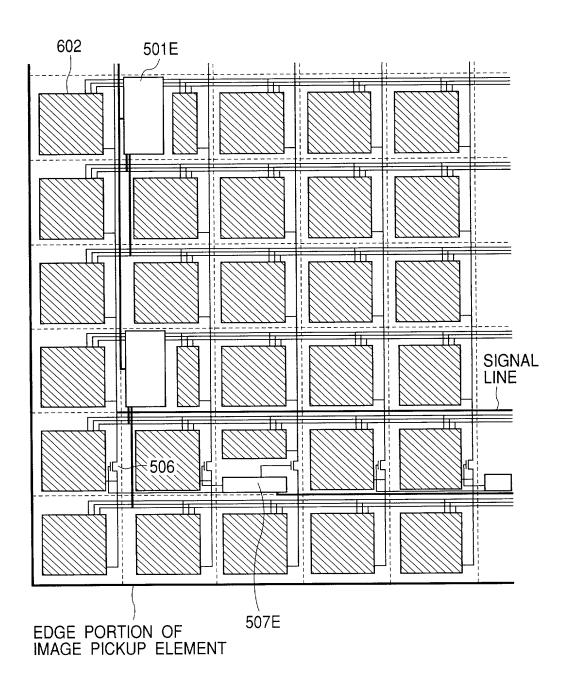


FIG. 15







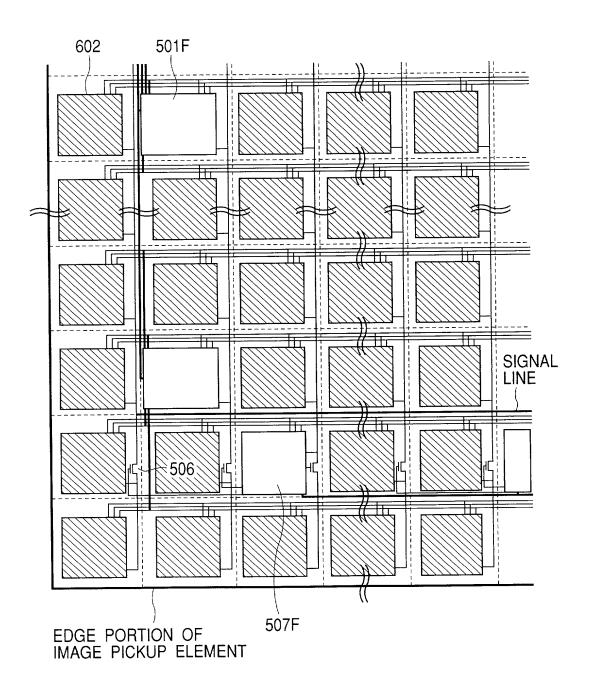


FIG. 19

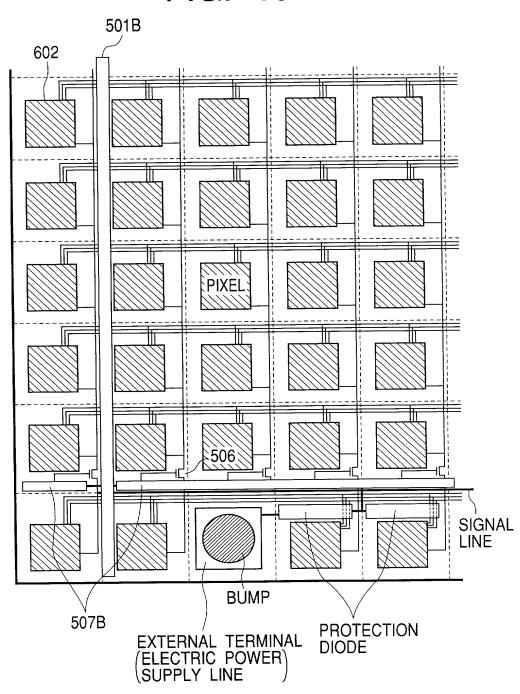


FIG. 20

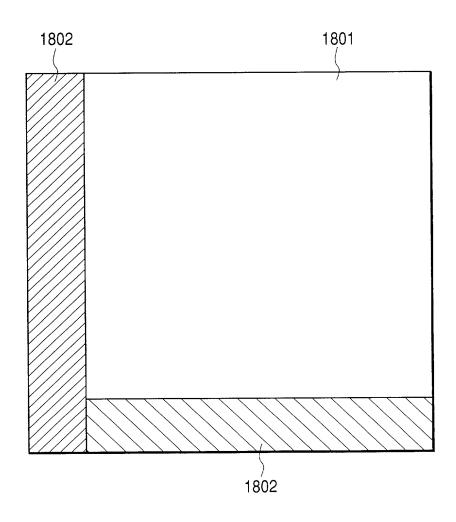


FIG. 21

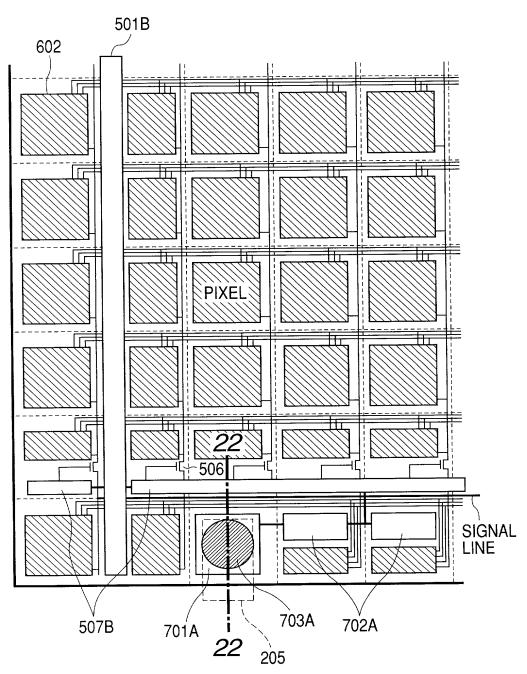


FIG. 22

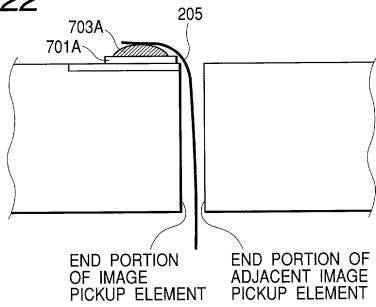


FIG. 23

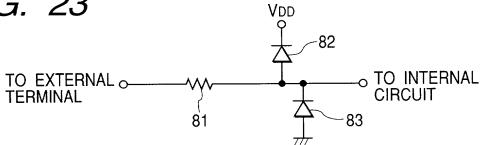
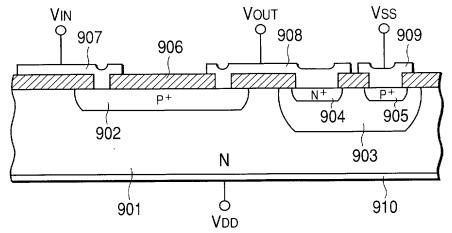
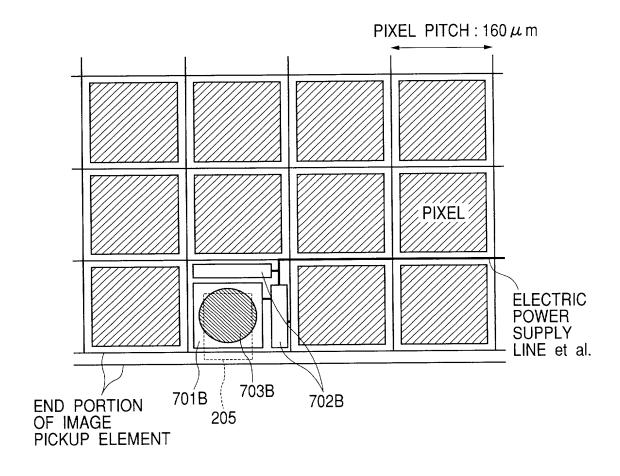


FIG. 24





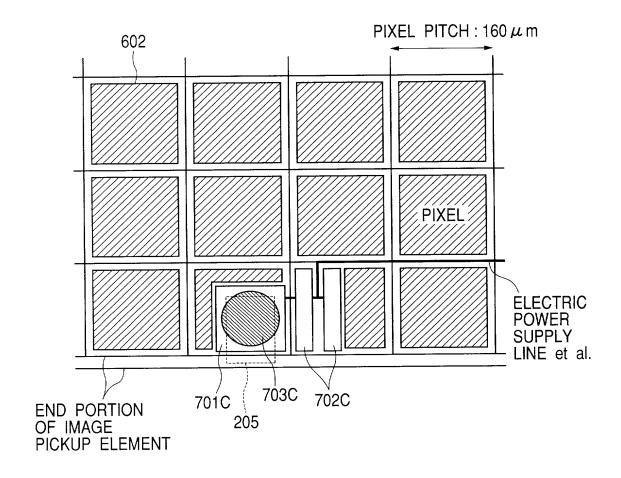


FIG. 27

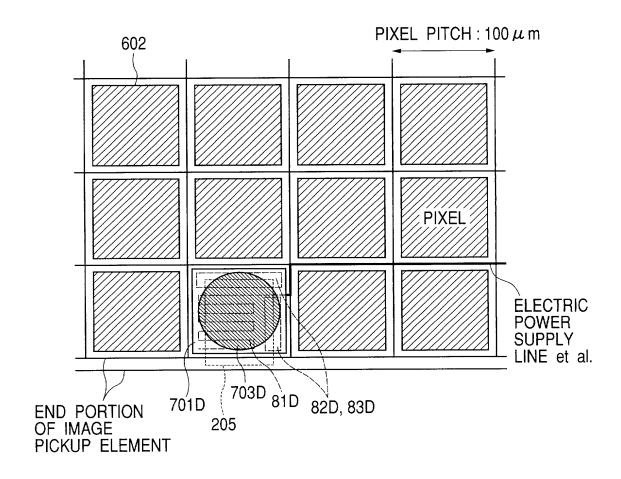


FIG. 28

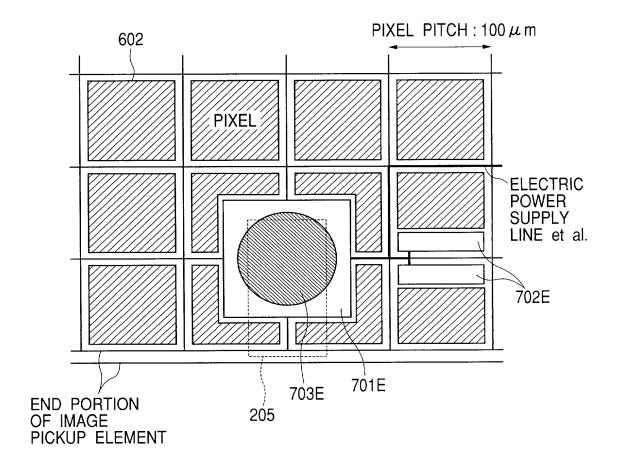


FIG. 29

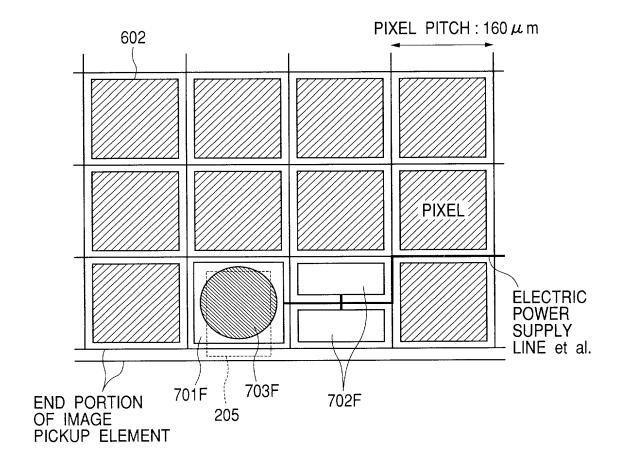


FIG. 30

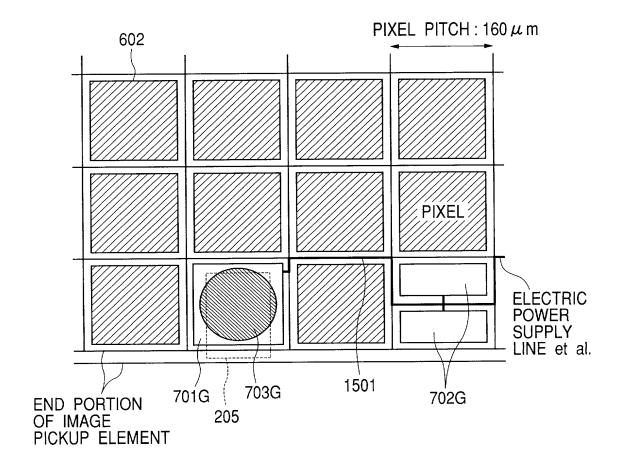


FIG. 31

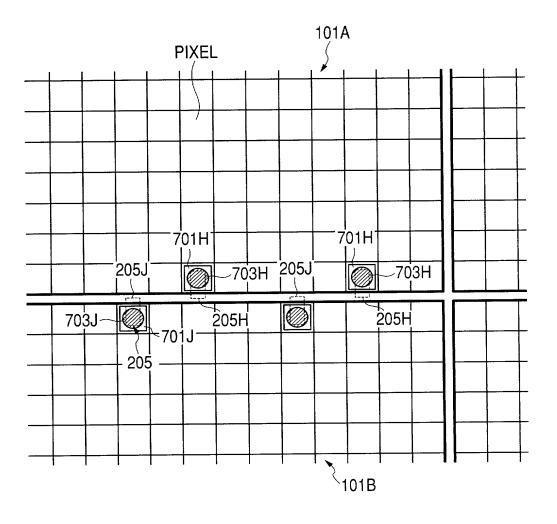
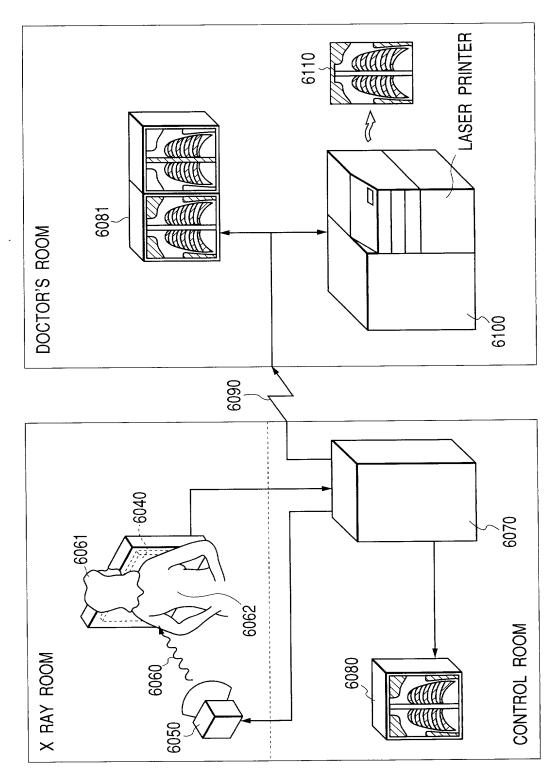


FIG. 32



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